

JFW



Docket No.: L&L-I0217

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

By: O K P. Siselman Date: February 22, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/620,093 Confirmation No: 8716  
Applicant : Jörg Berthold et al.  
Filed : July 15, 2003  
Art Unit : 2825  
Examiner : Phallaka Kik  
Title : Method for Determining the Critical Path of an Integrated Circuit  
Docket No. : L&L-I0217  
Customer No. : 24131

LETTER

Mail Stop: Missing Parts  
Hon. Commissioner for Patents,  
Alexandria, VA 22313-1450

Sir:

Enclosed herewith is the signed substitute declaration for the above mentioned non-provisional patent application.

Respectfully submitted,

O K P. Siselman  
KERRY P. SISELMAN  
REG. NO. 37,237

/mjb  
Date: February 22, 2006  
Lerner Greenberg Stemer LLP  
Post Office Box 2480  
Hollywood, FL 33022-2480  
Tel: 954.925.1100  
Fax: 954.925.1101